# CUSTOMER APPROVE

# **SPECIFICATION**

# FOR

# **DOUBLE LIN TFT-LCD MODULE**

Edition : Preliminary spec 1.0

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Product No. :M270HAN01.0

APPROVED	CHECKED	PREPARED
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# **Revision History**

Date	Rev.	Page	Old Description	New Description	Remark
2022-05-03	1.0	All	The specification was first issued		

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# **1.General Description**

This specification applies to the 27" inch wide Color a-Si TFT-LCD Module M270HAN01.0 The display supports the Full HD -1920(H) x 1080(V) screen format and 16.7M colors (RGB 8 bits+data inpu). The input interface is Dual channel LVDS and this module doesn't contain an driver board for backlights.

# **1.1.Display Characteristics**

The following items are characteristics summary on the table under 25°C condition:

V

ITEMS	Unit	SPECIFICATIONS
Screen Dingonal	[mm]	685.65(27")
Active Area	[mm]	597.6 (H) ×336.15 (V)
Pixels H x V	-	1920 x 3(RGB)×1080
Pixels Pitch	[um]	311.25(per one triad) × 311.25
Pixels Arrangement	-	R.G.B. Vertical island
Display Mode	-	VA Mode,Nomrally Black
White Luminance(Center)	[cd/m <sup>2</sup> ]	1000 (Тур.)
Contrast Ratio	-	1000 (Тур.)
Response Time	[msec]	20 (Typ.Gray to Gray)
Viewing Angle	[degree]	89/89/89
Outline Dimension	[mm]	630.0(H) x 368.2(V) x 11.5(D) (Typ.)
Electrical Interface	-	Dual Channel LVDS
Support Color	-	16.7M colors (RGB 6 - bits +Hi - FRC data)
Surface Treatment		Anti-Glare 3H
Temperature Range Operating	[oC]	0 to +50℃
Storage(Shipping)	[oC]	-20 to+60℃

### **1.2 Absolute Maximum Rating of Environment**

Symbol	Description	Min.	Max.	Unit	Remark
ТОР	Operating Temperature	0	+50	[°C]	Note 2-1
TGS	Glass surface temperature	0	+65	[°C]	Note 2-1 Function judged only
НОР	Operation Humidity	5	90	[%RH]	
тѕт	Storage Temperature	-20	+60	[°C]	Note 2-1
нѕт	Storage Humidity	5	90	[%RH]	

### Permanent damage may occur if exceeding the following maximum rating.

# Note 2-1 :Temperature and relative humidity range are shown as the below figure.

- 1. 90% RH Max(Ta≤39℃)
- 2. Max wet-bulb temperatuer at 39℃ or less.(Ta≤39℃)
- 3. No condensation



Operating Range

 $\times$ 

Storage Range

# **1.3.Optical Characteristics**



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- 1. Equipment setup: Please refer to *Note 2 2.*
- 2. Panel Lighting time: 30 minutes
- 3. VDD=5.0V, Fv=60Hz,Is=60mA,Ta=25°C

Symbol	Descriptio	on	Min.	Тур.	Max.	Unit	Remark
Lw	White Luminance (Cer	800	1000	-	[cd/m2]	Note 2 - 2 By SR - 3	
Luni	Luminance Uniformit	ty (9 points)	75	80	-	%	Note 2 - 3 By SR - 3
Crr	Contrast Ratio (Cente	er of screen)	700	1000	-	-	Note 2 - 4 By SR - 3
θR	Horizontal Viewing	Right	75	89	-		
θL	Angle (CR=10)	Left	75	89	-		
ΦН	Vertical Viewing	Up	75	89			Note 2 - 5 By SR - 3
ΦL	Angle(CR=10)	Down	75	89			
θR	Horizontal Viewing	Right	75	89		[degree]	
θL	Angle (CR=5)	Left	75	89	-		
ΦН	Vertical Viewing Angle	Up	75	89	-		
ΦL	(CR=5)	Down	75	89	-		
Тстс	Response Time	Gray To Gray		20	-	[msec]	Note 2 - 6 ByTRD - 100
Rx		Red x	0.593	0.623	0.653		
Ry		Red y	0.318	0.348	0.378		
Gx		Green x	0.274	0.304	0.334		
Gy	Color Coordinates	Green y	0.621	0.651	0.681	 -	
Bx	(CIE 1931)	Blue x	0.112	0.142	0.172		By SR - 3
By		Blue y	0.623	0.653	0.683		
Wx		White x	0.275	0.305	0.335		
Wy	T.	White y	0.342	0.372	0.402		
	NTSC Area Ratlo			72		[%]	By SR-3

# Note2-2:Equipment setup



Note 2-3: Luminance UniformityMeasurement

### **Definition:**

Luminance Uniformity = Maximum Luminance of 9Points(P1~P9) Maximum Luminance of 9Points(P1~P9)

# a.Test pattern: White Pattern



**Definition:** 

Contrast Ratio = Luminance of White pattern Luminance of Black pattern

a. Measured position: Center of screen (P5) & perpendicular to the screen ( $\theta=\Phi=0^{\circ}$ )

Note 2-5: Viewing angle measurement

**Definition:**The angle at which the contrast ratio is greater than 10& 5.

a. Horizontal view angle: Divide to left & right ( $\theta L \& \theta R$ )

Vertical view angle: Divideto up & down ( $\Phi H \& \Phi L$ )



### Note 2-6: Response time measurement

The output signals of photo detector are measured when the input signals are changed from "Gray level A" to "Gray level B" (faling time, TF), and from "Gray level B" to "Gray level A" (rising time, TR), respectively. The response time is interval between the 10% and 90% of optical response.



The output signals of photo detector are measured when the input signals are changed from "Gray level A"to"Gray level B"(falling time,TF),and from "Gray level B"to"Gray level A"(rising time,TR),respectively.The response time is interval between the10% and 90% of optical response.

The gray to gray response time is defined as the following table.

Gray Level to	o Gray Level	Target gray level				
		LO	L63	L127	L191	L255
Start gray level	L0					
level	L63					
	L127					
	L191					
	L255					

 $T_{GTG_{typ}}$  is the total average time at rising time and faling time of gray to gray

#### **1.4 Mechanical Characteristics**

Symbol	Description	Min.	Max.	Unit	Remark
Pbc	<b>Backside Compression</b>	2.5	-	[Kgf]	Note 2-8

### Note 2-8: Test Method

The point is at a distance from right-downside 25mm x 25mm defined as the Start Point of Measure Points, and the point is at a distance 25mm from left-side & around 10mm from PCB defined as the End Point

Align 50mm x 50mm block from Start Point on the Bezel Back, and the corners of each block are Measure Points

If the distance from the last block to each side of the End Point  $\ge 25$  mm, add other block to make sure that most area of Bezel Back can be measured.



# 2 TFT-LCD Module

### 2.1 Block Diagram

The following shows the block diagram of the 27 inch Color TFT\_LCD Module



## 2.2 Interface Connection 2.2.1 Connector Type

TFT-LCD Connector	Manufacturer	P-TWO	STM
IFI-LCD Connector	Part Number	AL230F-A0GID-P	093G30-0200IA-M4
Mating Connector	Manufacture	JAE or Compatible	
Mating Connector	Part Number	FI-X30HL (Locked Type)	

# 2.2. 2 LCD Connector Pin Assignment

1         RxOINO-         Negative LDVS differential data input (Odd data)           2         RxOIN0+         Positive LDVS differential data input (Odd data)           3         RxOIN1+         Negative LDVS differential data input (Odd data)           4         RxOIN1+         Positive LDVS differential data input (Odd data)           5         RxOIN2-         Negative LDVS differential data input (Odd data, H-Sync,V-Sync,DSPTMG)           6         RxOIN2-         Positive LDVS differential data input (Odd data, H-Sync,V-Sync,DSPTMG)           7         GND         Power Ground           8         RxOCLKIN+         Negative LDVS differential data input (Odd data, H-Sync,V-Sync,DSPTMG)           9         RxOLKIN+         Negative LDVS differential data input (Odd clock)           9         RxOLKIN+         Positive LDVS differential data input (Odd data)           10         RxOIN3-         Negative LDVS differential data input (Odd data)           11         RxOIN3+         Positive LDVS differential data input (Even data)           12         RxEIN0-         Negative LDVS differential data input (Even data)           13         RxEIN1+         Positive LDVS differential data input (Even data)           14         GND         Power Ground           15         RxEIN1+         Positive LDVS differential data input (Even	PIN#	Symbol	DESCRIPTION
3       RxOIN1-       Negative LDVS differential data input (Odd data)         4       RxOIN1+       Positive LDVS differential data input (Odd data)         5       RxOIN2-       Negative LDVS differential data input (Odd data,H-Sync,V-Sync,DSPTMG)         6       RxOIN2-       Positive LDVS differential data input (Odd data,H-Sync,V-Sync,DSPTMG)         7       GND       Power Ground         8       RxOCLKIN-       Negative LDVS differential data input (Odd clock)         9       RxOIX3-       Negative LDVS differential data input (Odd data)         10       RxOIN3-       Negative LDVS differential data input (Odd data)         11       RxOIN3-       Negative LDVS differential data input (Odd data)         12       RxEIN0-       Negative LDVS differential data input (Even data)         13       RxEIN0+       Positive LDVS differential data input (Even data)         14       GND       Power Ground         15       RxEIN1+       Negative LDVS differential data input (Even data)         16       RxEIN1+       Positive LDVS differential data input (Even data)         17       GND       Power Ground         18       RxEIN2+       Negative LDVS differential data input (Even data)         20       RXECLKIN+       Negative LDVS differential data input (Even data)	1	RxOIN0-	Negative LDVS differential data input (Odd data)
4         RxOIN1+         Positive LDVS differential data input (Odd data)           5         RxOIN2-         Negative LDVS differential data input (Odd data,IH-Sync,V-Sync,DSPTMG)           6         RxOIN2-         Positive LDVS differential data input (Odd data,IH-Sync,V-Sync,DSPTMG)           7         GND         Power Ground           8         RxOCLKIN-         Negative LDVS differential data input (Odd clock)           9         RxOCLKIN+         Positive LDVS differential data input (Odd data)           10         RxOIN3-         Negative LDVS differential data input (Odd data)           11         RxOIN3+         Positive LDVS differential data input (Odd data)           12         RxEIN0-         Negative LDVS differential data input (Even data)           13         RxEIN0+         Positive LDVS differential data input (Even data)           14         GND         Power Ground           15         RxEIN1+         Negative LDVS differential data input (Even data)           16         RxEIN1+         Positive LDVS differential data input (Even data)           19         RxEIN2+         Negative LDVS differential data input (Even data)           20         RXECLKIN+         Negative LDVS differential data input (Even data)           21         RXECLKIN+         Negative LDVS differential data input (Even data)	2	RxOIN0+	Positive LDVS differential data input (Odd data)
5         RxOIN2-         Negative LDVS differential data input (Odd data, H-Sync,V-Sync,DSPTMG)           6         RxOIN2-         Positive LDVS differential data input (Odd data, H-Sync,V-Sync,DSPTMG)           7         GND         Power Ground           8         RXOCLKIN-         Negative LDVS differential data input (Odd clock)           9         RXOCLKIN+         Positive LDVS differential data input (Odd data)           10         RXOIN3-         Negative LDVS differential data input (Odd data)           11         RxOIN3+         Positive LDVS differential data input (Odd data)           11         RxOIN3+         Positive LDVS differential data input (Odd data)           12         RxEIN0-         Negative LDVS differential data input (Even data)           13         RxEIN0+         Positive LDVS differential data input (Even data)           14         GND         Power Ground           15         RxEIN1+         Negative LDVS differential data input (Even data)           17         GND         Power Ground           18         RxEIN2+         Negative LDVS differential data input (Even data)           20         RXECLKIN+         Negative LDVS differential data input (Even clock)           21         RXECLKIN+         Positive LDVS differential data input (Even clock)           22	3	RxOIN1-	Negative LDVS differential data input (Odd data)
6         RX0IN2-         Positive LDVS differential data input (Odd data,H-Sync,V-Sync,DSPTMG)           7         GND         Power Ground           8         RXOCLKIN-         Negative LDVS differential data input (Odd clock)           9         RXOCLKIN+         Positive LDVS differential data input (Odd clock)           10         RXOLKIN+         Positive LDVS differential data input (Odd data)           11         RXOIN3-         Negative LDVS differential data input (Odd data)           12         RXEIN0-         Negative LDVS differential data input (Odd data)           13         RxEIN0-         Negative LDVS differential data input (Even data)           14         GND         Power Ground           15         RXEIN1-         Negative LDVS differential data input (Even data)           16         RXEIN1+         Positive LDVS differential data input (Even data)           17         GND         Power Ground           18         RxEIN2-         Negative LDVS differential data input (Even data)           20         RXECLKIN-         Negative LDVS differential data input (Even clock)           21         RXECLKIN-         Negative LDVS differential data input (Even clock)           22         RXEIN3-         Negative LDVS differential data input (Even data)           23         RXEIN3	4	RxOIN1+	Positive LDVS differential data input (Odd data)
7GNDPower Ground8RxOCLKIN-Negative LDVS differential data input (Odd clock)9RxOCLKIN+Positive LDVS differential data input (Odd clock)10RxOIN3-Negative LDVS differential data input (Odd data)11RxOIN3+Positive LDVS differential data input (Odd data)12RxEIN0-Negative LDVS differential data input (Even data)13RxEIN0+Positive LDVS differential data input (Even data)14GNDPower Ground15RxEIN1-Negative LDVS differential data input (Even data)16RxEIN1+Positive LDVS differential data input (Even data)17GNDPower Ground18RxEIN2-Negative LDVS differential data input (Even data)19RxEIN2+Positive LDVS differential data input (Even data)20RXECLKIN+Negative LDVS differential data input (Even data)21RXECLKIN-Negative LDVS differential data input (Even data)22RxEIN3-Negative LDVS differential data input (Even data)23RXEIN3+Positive LDVS differential data input (Even data)24GNDPower Ground25NCNo connection (For AUO test only.Do not connect)26NCNo connection (For AUO test only.Do not connect)27NCNo connection (For AUO test only.Do not connect)28VDD+5.0V Power Supply29VDD+5.0V Power Supply	5	RxOIN2-	Negative LDVS differential data input (Odd data,H-Sync,V-Sync,DSPTMG)
8       RxOCLKIN-       Negative LDVS differential data input (Odd clock)         9       RxOCLKIN+       Positive LDVS differential data input (Odd clock)         10       RxOIN3-       Negative LDVS differential data input (Odd data)         11       RxOIN3+       Positive LDVS differential data input (Odd data)         12       RxEIN0-       Negative LDVS differential data input (Even data)         13       RxEIN0+       Positive LDVS differential data input (Even data)         14       GND       Power Ground         15       RxEIN1-       Negative LDVS differential data input (Even data)         16       RxEIN1+       Positive LDVS differential data input (Even data)         17       GND       Power Ground         18       RxEIN2-       Negative LDVS differential data input (Even data)         19       RxEIN2+       Positive LDVS differential data input (Even data)         20       RXECLKIN-       Negative LDVS differential data input (Even data)         21       RXECLKIN+       Positive LDVS differential data input (Even clock)         22       RxEIN3-       Negative LDVS differential data input (Even clock)         23       RXEIN3+       Positive LDVS differential data input (Even data)         24       GND       Power Ground         25	6	RxOIN2-	Positive LDVS differential data input (Odd data,H-Sync,V-Sync,DSPTMG)
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11RxOIN3+Positive LDVS differential data input (Odd data)12RxEIN0-Negative LDVS differential data input (Even data)13RxEIN0+Positive LDVS differential data input (Even data)14GNDPower Ground15RxEIN1-Negative LDVS differential data input (Even data)16RxEIN1+Positive LDVS differential data input (Even data)17GNDPower Ground18RxEIN2-Negative LDVS differential data input (Even data)19RxEIN2+Positive LDVS differential data input (Even data)20RXECLKIN-Negative LDVS differential data input (Even data)21RXECLKIN+Positive LDVS differential data input (Even clock)22RXECLKIN+Negative LDVS differential data input (Even clock)23RXEIN3+Positive LDVS differential data input (Even data)24GNDPower Ground25NCNo connection (For AUO test only.Do not connect)26NCNo connection (For AUO test only.Do not connect)27NCNo connection (For AUO test only.Do not connect)28VDD+5.0V Power Supply29VDD+5.0V Power Supply	9	RxOCLKIN+	Positive LDVS differential data input (Odd clock)
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23RxEIN3+Positive LDVS differential data input (Even data)24GNDPower Ground25NCNo connection (For AUO test only.Do not connect)26NCNo connection (For AUO test only.Do not connect)27NCNo connection (For AUO test only.Do not connect)28VDD+5.0V Power Supply29VDD+5.0V Power Supply	21	RXECLKIN+	Positive LDVS differential data input (Even clock)
24GNDPower Ground25NCNo connection (For AUO test only.Do not connect)26NCNo connection (For AUO test only.Do not connect)27NCNo connection (For AUO test only.Do not connect)28VDD+5.0V Power Supply29VDD+5.0V Power Supply	22	RxEIN3-	Negative LDVS differential data input (Even data)
25NCNo connection (For AUO test only.Do not connect)26NCNo connection (For AUO test only.Do not connect)27NCNo connection (For AUO test only.Do not connect)28VDD+5.0V Power Supply29VDD+5.0V Power Supply	23	RxEIN3+	Positive LDVS differential data input (Even data)
26     NC     No connection (For AUO test only.Do not connect)       27     NC     No connection (For AUO test only.Do not connect)       28     VDD     +5.0V Power Supply       29     VDD     +5.0V Power Supply	24	GND	Power Ground
27     NC     No connection (For AUO test only.Do not connect)       28     VDD     +5.0V Power Supply       29     VDD     +5.0V Power Supply	25	NC	No connection (For AUO test only.Do not connect)
28     VDD     +5.0V Power Supply       29     VDD     +5.0V Power Supply	26	NC	No connection (For AUO test only.Do not connect)
29 VDD +5.0V Power Supply	27	NC	No connection (For AUO test only.Do not connect)
	28	VDD	+5.0V Power Supply
30     VDD     +5.0V Power Supply	29	VDD	+5.0V Power Supply
	30	VDD	+5.0V Power Supply

# 2.3Electrical characteristics

# 2.3.1 Absolute MaxImum Rating

Permanent damage may occur if exceeding the following maximum rating.

Symbol	Description	Min.	Max.	Unit.	Remark
VDD	Power Supply Input Voltage	GND-0.3	6.0	[Volt]	Ta=25 ℃

## 2.3.2Recommeomded Operating Condition

Symble	Description	Min.	Тур.	Max.	Unit	Remark
VDD	Power Supply Input Voltage	4.5	5.0	5.5	[Volt]	
IDD	Power Supply	-	0.93	1.22	[A]	VDD=5V, white Pattern,FV= 60Hz
	Power Supply Input Current		1.22	1.46	[A]	VDD=5V, white Pattern,FV= 75Hz
IRush	Input Current	-	-	3.0	[A]	Note 3-1
PDD	VDD Power	-	4.65	6.1	[Watt]	VDD=5V, white Pattern,FV= 60Hz
FDD	Cornsumption		6.10	7.30	[Watt]	VDD=5V, white Pattern,FV= 70Hz
VDDrp	Allowable VDD Ripple Voltage	-	-	500	[mV]	VDD=5V, white Pattern,FV= 75Hz

Note 3-1: Inrush Current measurement

# Test circuit:



# The duration of VDD rising time: 470us.

# 2.4 Signal Characteristics2.4.1 LCD Pixel Format



### 2.4.2 LVDS Data Format



#### Note 3-2

a. O = "Odd Pixel Data" E = "Even Pixel Data"

b. Refer to 2.4.1 LCD pixel format, the I st data is I(Odd Pixel Data), the 2<sup>nd</sup> data is 2(Even Pixel Data) And the last data is I920(Even Pixel Data).

### 2.4.3 Color versus Input Data

### The following table is for color versus input data(8bit). The higher the gray level, the bringhter the color

Color	Gray Level	Color Input Data																								
		RED data ( <b>MSB</b> :R7, <b>LSB</b> :R0)					GREEN data (MSB:G7, LSB:G0)					BLUE data (MSB:B7, LSB:B0)					Remark									
		R7	R6	R5	R4	R3	R2	R1	RO	G7	G6	G5	G4	G3	G2	G1	GO	B7	B6	B5	B4	B3	B2	B1	BO	
Black		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
White	-	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
Gray 127	-	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	0	1	1	1	1	1	1	1	
	ю	0	0	0	0	Ó	0	0	0	0	0	0	0	0	0	0	0	O	0	O	0	O	0	O	O	Black
Red	:		3	:	1	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	1	:	
	L255	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	O	0	
	LO	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
Green	:			3	ţ.		:	1	3	4	4	4		:	- 2	ţ.		1	1	:	2	:	1	1	2	
	L255	0	O	0	0	0	0	0	0	1	1	1	1	1	1	1	1	O	0	0	0	0	0	0	O	
	LO	0	O	0	O	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	Black
Blue	:	2		1	:	:	:	:	1	:	1	:	:	:	:	1	:	1	1	:	2	1	1	1	÷	
	L255	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	

### 2.4.4 LVDS Specification

### **DC Characteristics:**

Symbol	Description	Min	Тур	Max	Units	Condition
V <sub>TH</sub>	LVDS Differential Input High Threshold	-	-	+100	[mV]	V <sub>CM</sub> =1.2V
V <sub>TL</sub>	LVDS Differential Input High Threshold	-100	-	-	[mV]	V <sub>CM</sub> =1.2V
	LVDS Differential Input High Voltage	100	-	600	[mV]	
Vсм	LVDS Common Mode Voltage	+1.0	+1.2	+1.5	[V]	$V_{TH}-V_{TL}=200 mV$

### **3** Backlight Unit

### **3.1 Block Diagram**

The following shows the block diagram of the 27 inch Backlight Unit, And it includes 72pcs LED in the LED light bar.(8 strings and 9 pcs LED one string).

(Ta=25°C)



Item	Symbol	Min.	Тур.	Max.	Unit
LED operation Voltage	${f V}_{{ m led}}$	47.2	-	52	V
LED operation Curent	I led	_	900	_	mA
BackLinght Power	P BL	42.48	-	46.8	W
Luminance	L	800	1000		nit
LED Life Time		30000			Hrs
Luminance uniformity	$\triangle L$	75	80		%

# 3.3Backlight Electrical / Optical Characteristics

Backlight

connector CN2 : PH-

2P \*2

Pin	Signal Name
1	VDD- (Black)
2	VDD+ (Red)



**4 Reliability Test** 

AUO reliability test items are listed ad following table.(Bare panel only)

Ltems	Condition	Remark
Temperature Humidity Bias(THB)	Ta= 50°C, 80%RH, 300huurs	
High Temperature Operation(HTO)	Ta= 50℃, 50%RH, 300huurs	
Low Temperature Operation(LTO)	$Ta=0^{\circ}C$ , 300huurs	
High Temperature Storage (HTS)	Ta=60℃, 300huurs	
Low Temperature Storage (LTS)	Ta = -20°C, 300 huurs	
Vibration Test (Non-operation)	Acceleration: 1.5 Grms Wave: Random Frequency:10-200 Hz Sweep:30 Minutes each Axis (X,Y,Z)	
Shock Test (Non-operation)	Acceleration: 50Grms Wave: Half-sine Active TIMIE: 20ms Direction : $\pm X, \pm Y, \pm Z$ (one time for each Axis)	
Thermal Shock Test(TST)	-20°C/30min, 60°C/30min,100 cycles	
On/Off Test	On/10sec,Off/10sec,30,000 cycles	Note 4-1
ESD(Elector Static Dishcharge)	Contact Discharge: $\pm$ 15KV, 150pF(330 $\Omega$ ) 1sec, 8 points, 25 times/point Air Discharge: $\pm$ 15KV, 150pF(330 $\Omega$ ) 1sec, 8 points, 25 times/point	Note 4-2
Altitude Test	Operation:18,000 ft Non-Operation:40,000ft	

Note 4-1 a. A cycle of rapid temperature change consists of varying the temperature fron -20°C to 60°C, And back again. Power is not applied during the test.

b. After finish temperature cycling, the unit is placed in normal room ambient for at least 4 hours before power on.

Note 4-2 EN61000-4-2, ESD class B: Certain performance degradtion allowed

No data lost Self-recoverable No hardware failures. 5.Shipping Label

### The label is on the panel as shown below



玻璃厂商代码+尺

Parameter	Packing box	Unit
Size	6 7 0 ( L) 282( W) x4 1 2 ( H) ( typ. )	mm
Weight	3.1(typ.)	kg
Total weight	3 1 (typ.) (with 10 products)	kg



### 6.PRECAUTION

### 6.1 ASSEMBLY AND HANDLING PRECAUTIONS

- 1. Do not apply rough force such as bending or twisting to the module during assembly.
- 2. To assemble or install module into user's system can be in clean working areas. The dust and oil may cause electrical short or worsen the polarizer.
- 3 . It's not permitted to have pressure or impulse on the module because the LCD panel and Bac klight will will be damaged.
- 4. Always follow the correct power sequence when LCD module is connecting and operating. This can prevent damage to the CMOS LSI chips during latch-up.
- 5. Do not pull the I/F connector in or out while the module is operating . 6 6. Do not disassemble themodule. Use a soft dry cloth without chemicals for cleaning, because the surface of polarizer is very
- 7. soft and easily scratched.
- 8. It is dangerous that moisture come into or contacted the LCD module, because moisture may damage LCD module when it is operating.
- 9. High temperature or humidity may reduce the performance of module. Please store LCD module within the specified storage conditions.
- 10. When ambient temperature is lower than 10  $^{\circ}$ C may reduce the display quality. For example, the response time will become slowly.

### 6.2 SAFETY PRECAUTIONS

- 1 It is dangerous that moisture come into or contacted the LCD module, because the moisture may damage LCD module when it is operating.
- 2 If the liquid crystal material leaks from the panel, it should be kept away from the eyes or mouth, in case of contact with hands, skin or clothes, it has to be washed away thoroughly with soap.
- 3 After the modlule's end of life, it is not harmful in case of normal operation and stora.

